means includes a multiplying digital to analog converter.

5. The digitally synthesized source according to claim 1 wherein said clock means includes an internal clock.

6. The digitally synthesized source according to claim 1 wherein said clock means includes an external clock.

7. In a digitally synthesized alternating current voltage source for synthesizing a sinusoidal voltage waveform including a read only memory storing digital values representing sine waveforms; a first multiplying digital to analog converter connected to said read only memory through a first latch means; a second multiplying digital to analog converter connected to said read only memory through a second latch means; an operational amplifier having a inverting input, a non-inverting input and an output; switch means for alternatively connecting said first multiplying digital to analog converter and said second multiplying digital to analog converter to said inverting input and said non-inverting input such that one of said first multiplying digital to analog converter and said second multiplying digital to analog converter is connected to said inverting input while the other is connected to said non-inverting input; and clock means connected to said switch means for controlling connection of said first and second multiplying digital to analog converters to said inverting input; the improvement comprising:

a feedback connection between the output and the inverting input of said operational amplifier through a variable capacitor which is set to a low capacitance value for eliminating frequency components above 10-20 MHZ in the voltage waveform and to flatten the RMS gain of the voltage waveform within the audiofrequency range of the waveform.

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